

FIG. 1 is a schematic diagram of a frame structure in a packet-based communication system. The frame structure is shown as a sequence of slots, each containing a packet. The slots are labeled as follows: Frame n-1, Frame n, and Frame n+1. The slots are further divided into sub-slots: CBR Slot, VBR Slot, SCR Slot, and ABR Slot. The slots are also labeled with their respective bandwidths: R-type Bandwidth, C-type Bandwidth, and ABR Slot. The slots are separated by a Movable Boundary, which is indicated by a dashed line. The slots are also labeled with their respective control fields: Guard Time, SYNC And Other Control Fields, ATM Cell (Packet), and Guard Time. The slots are also labeled with their respective control fields: Guard Time, SYNC And Other Control Fields, ATM Cell (Packet), and Guard Time.

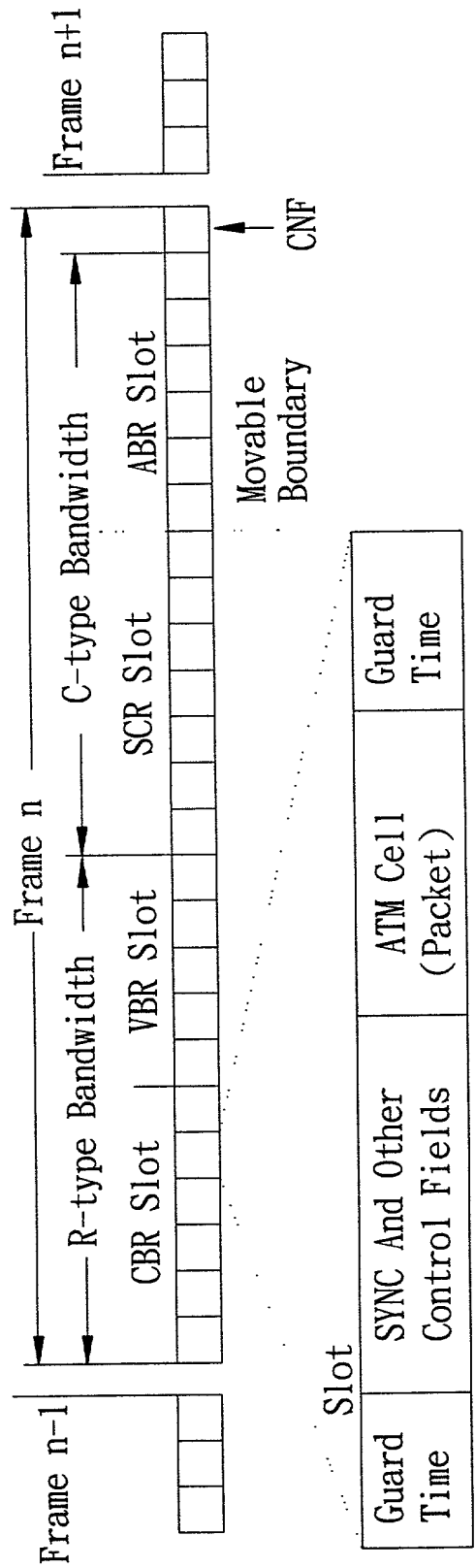


FIG. 1

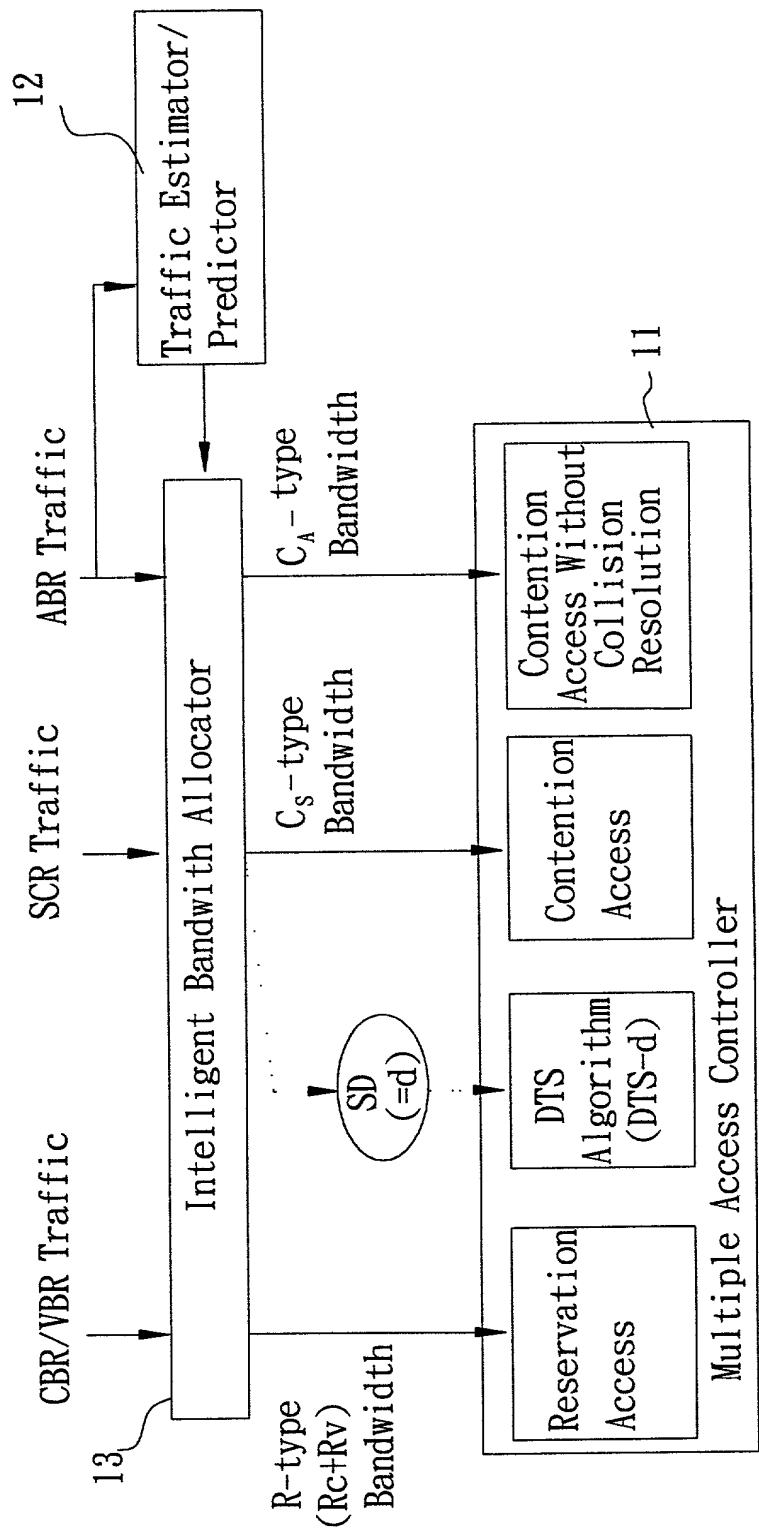


FIG. 2

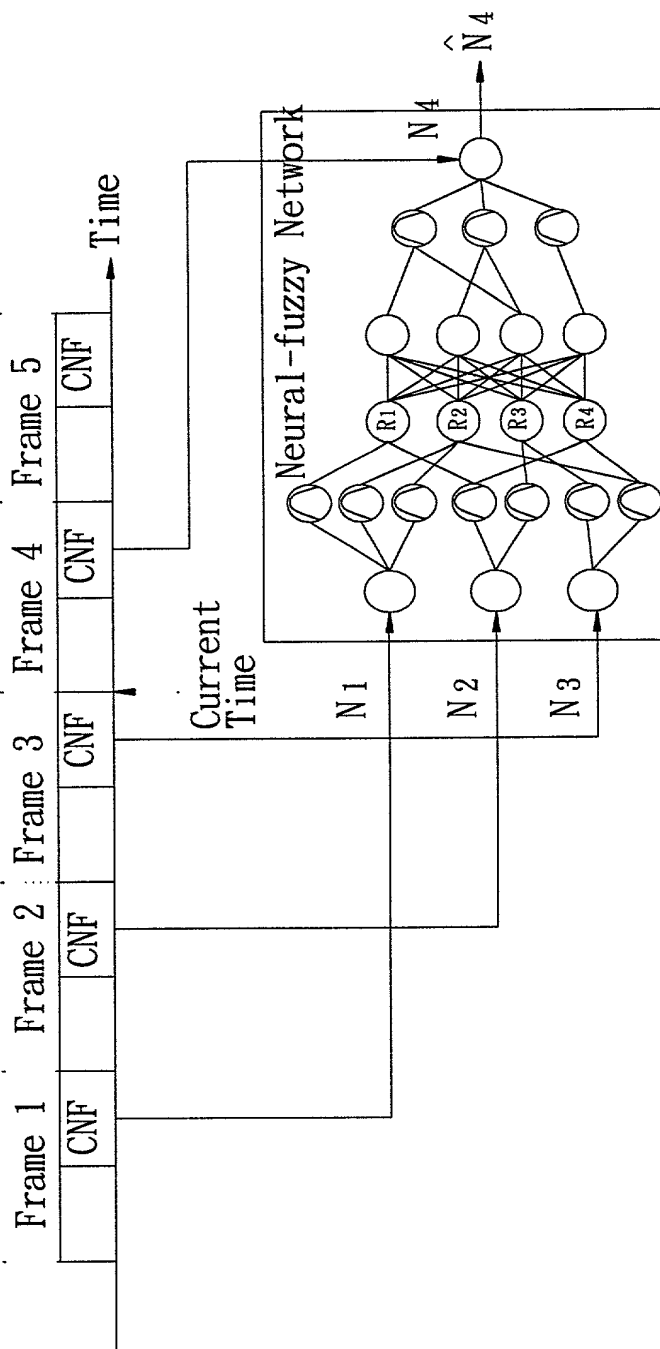


FIG. 3

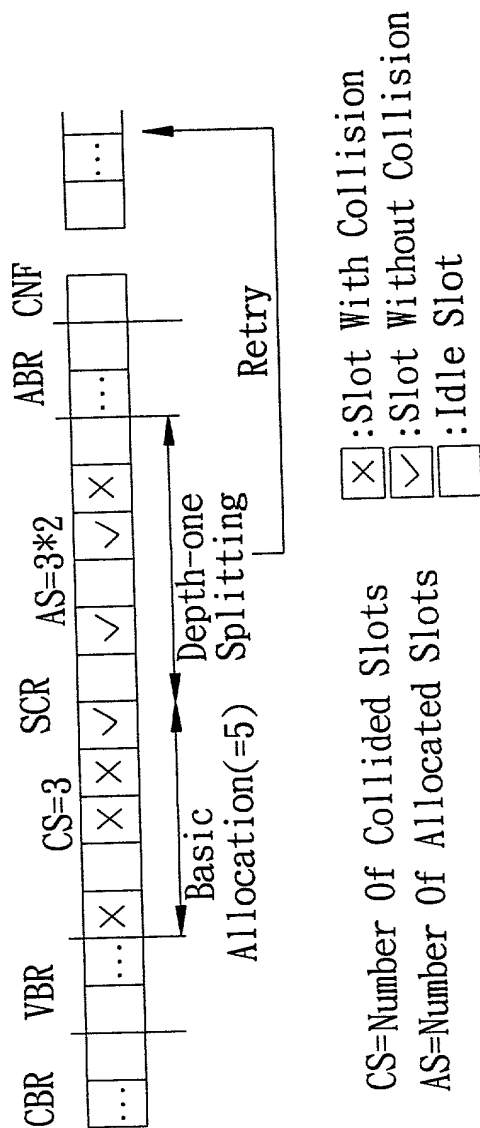


FIG. 4